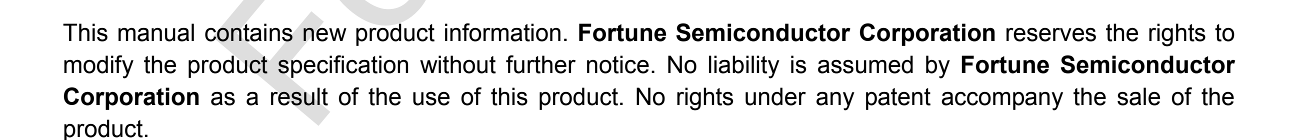
Datasheet

DW01M

One Cell Lithium-ion/Polymer Battery Protection IC With Built-in MOSFET



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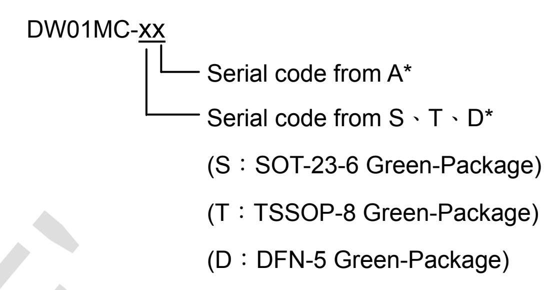
1. General Description

The DW01M battery protection IC with built-in N-MOSFET is designed to protect lithium-ion /polymer battery from damage or degrading the lifetime due to overcharge, overdischarge, and/or overcurrent for one-cell lithium-ion/polymer battery powered systems, such as cellular phones.
The ultra-small package and less required external components make it ideal to integrate the DW01M into the limited space of battery pack. The accurate ±50mV overcharging detection voltage ensures safe and full utilization charging. The very low standby current drains little current from the cell while in storage.

2. Features

- **Built-in N-MOSFET of low turn-on resistance.**
- **Reduction in Board Size due to Miniature Package TSSOP-8 or SOT-23-6 or DFN-5.**
- **Protection IC：**
  - **Ultra-Low Quiescent Current at 3μA (Vcc=3.9V).**
  - **Overcharge Protection Voltage**
    4.3V ± 50mV (DW01MC-X)
  - **Overcharge Protection Voltage**
    4.28V ± 70mV (DW01MC-XA)
  - **Overdischarge Protection Voltage**
    2.4V ± 100mV
    (DW01MC-X, DW01MC-XA)
  - **Overcurrent Protection Voltage**
    150mV ± 30mV
    (DW01MC-X, DW01MC-XA)
  - **Auto Recovery function**
- **MOSFET：**
  - **R_{SS}(ON) ＜ 55mΩ**
    (V_{GS} = 3.7V , ID = 1A)
    (DW01MC-X)
  - **R_{SS}(ON) ＜ 60mΩ**
    (V_{GS} = 3.7V , ID = 1A)
    (DW01MC-XA)

3. Ordering Information



* Refer to the product name list on next page.

TEMPERATURE RANGE
-40°C~+85°C

4. Applications

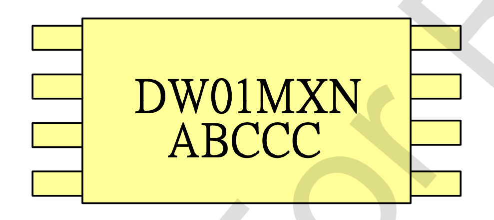
- **Protection IC for One-Cell Lithium-Ion / Lithium-Polymer Battery Pack**

5. Product Name List

Model	Overcharge detection voltage [VOCP] (V)	Overcharge release voltage [VOCR] (V)	Overdischarge detection voltage [VODP] (V)	Overdischarge release voltage [VODR] (V)	Overcurrent detection voltage [VOI1] (mV)	0V change function	Standby function release
DW01MC-S	4.30±0.050	4.100±0.050	2.40±0.100	3.0±0.100	150±30	NO	Auto
DW01MC-T	4.30±0.050	4.100±0.050	2.40±0.100	3.0±0.100	150±30	NO	Auto
DW01MC-D	4.30±0.050	4.100±0.050	2.40±0.100	3.0±0.100	150±30	NO	Auto
DW01MC-SA	4.28±0.070	4.080±0.070	2.40±0.100	3.0±0.100	150±30	NO	Auto
DW01MC-TA	4.28±0.070	4.080±0.070	2.40±0.100	3.0±0.100	150±30	NO	Auto
DW01MC-DA	4.28±0.070	4.080±0.070	2.40±0.100	3.0±0.100	150±30	NO	Auto

6. Pin Configuration and Package Marking Information

Pin No.	Symbol	Description
1	GND	Ground pin
2	GND	Ground pin
3	VCC	Power supply, through a resistor (R1)
4	VCC	Power supply, through a resistor (R1)
5	CS	Input pin for current sense, charger detect
6	BATT-	Connect to negative of charger or load
7	BATT-	Connect to negative of charger or load
8	D12	Two MOSFET common drain connection pin

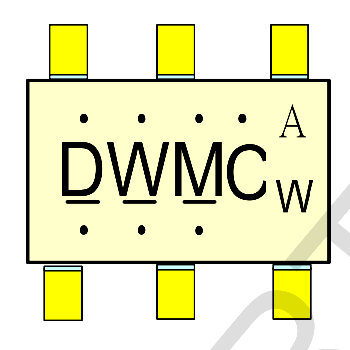


X：Serial Code, C, D
N：Serial Code, A
A：Year Code, A~Z & A ~ Z.
B：Week Code, A~Z & A ~ Z
CCC：Lot Code

TSSOP-8
Top View

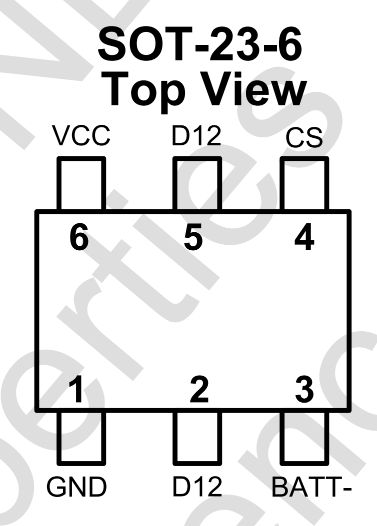
GND 1 | 8 D12
GND 2 | 7 BATT-
VCC 3 | 6 BATT-
VCC 4 | 5 CS

Pin No.	Symbol	Description
1	GND	Ground pin
2	D12	Two MOSFET common drain connection pin
3	BATT-	Connect to negative of charger or load
4	CS	Input pin for current sense, charger detect
5	D12	Two MOSFET common drain connection pin
6	VCC	Power supply, through a resistor (R1)



Top Point and Under_line：Lot No.
Bottom Point：Year
W : week, A~Z & A ~ Z
A：Serial code, A

SOT-23-6
Top View



Pin No.	Symbol	Description
1	NC	NC
2	GND	Ground pin
3	BATT-	Connect to negative of charger or load
4	VCC	Power supply, through a resistor (R1)
5	CS	Input pin for current sense, charger detect
6	D12	Tow MOSFET common drain connection pin

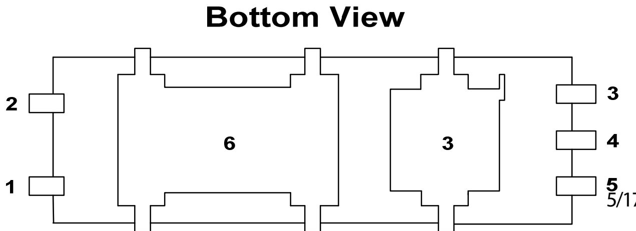
DW01MXAB001

X：Serial Code, A~D
A：Year.
B：Week Code, A~Z & A ~ Z
001 :Serial number

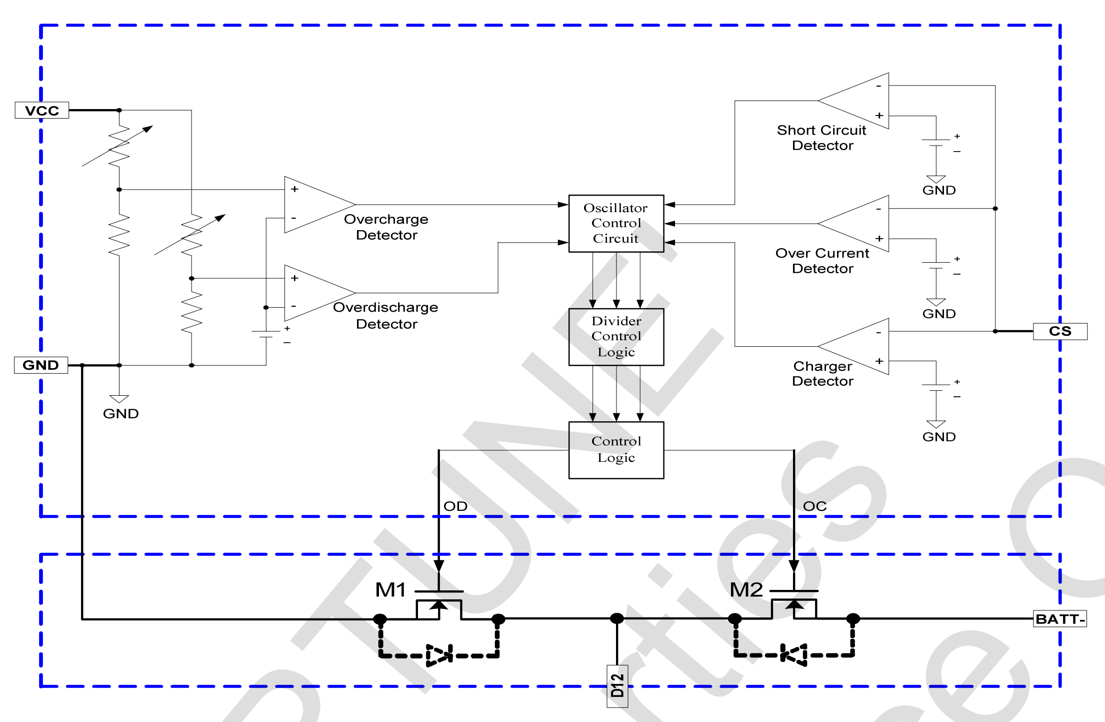
Top View



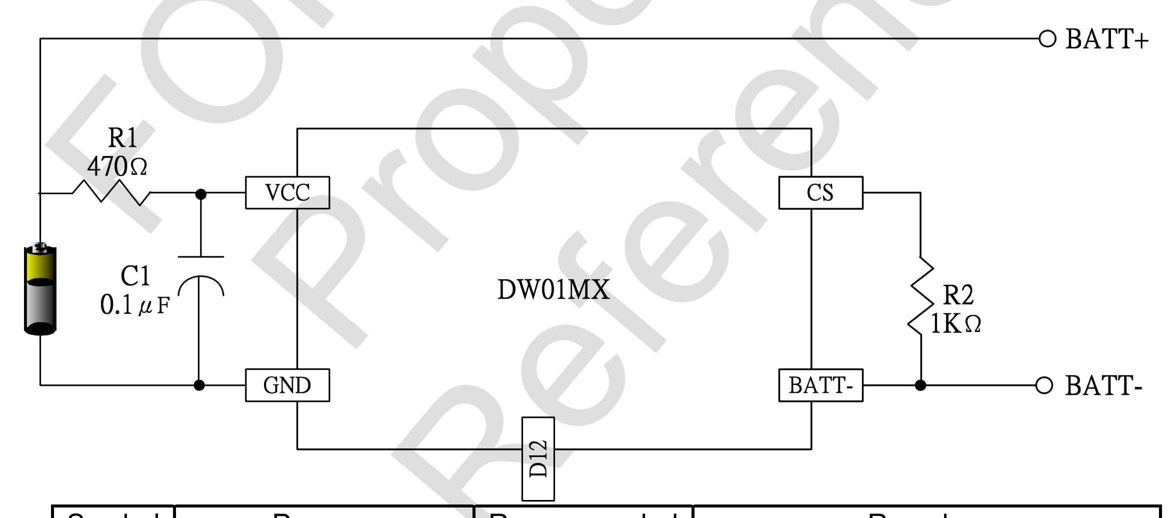
Bottom View



7. Functional Block Diagram



8. Typical Application Circuit



Symbol	Purpose	Recommended	Remakes
R1	ESD protection. For power fluctuation.	100~470Ω	Resistance should be as small as possible to avoid lowering of the overcharge detection accuracy caused by VDD pin current. Use 470Ω for better ESD protection.
C1	For power fluctuation.	0.1μF	
R2	Protection for reverse connection of a charger.	1k~2kΩ	Select a resistance as large as possible to prevent large current when a charge is connected in reverse.

9. Absolute Maximum Ratings

(GND=0V, Ta=25°C unless otherwise specified)

Item	Symbol	Rating	Unit
Input voltage between VCC and GND *Note1	VCC	GND-0.3 to GND+10	V
CS input pin voltage	VCS	VCC -20 to VCC +0.3	V
Operating Temperature Range	TOP	-40 to +85	°C
Storage Temperature Range	TST	-40 to +125	°C
Drain-Source Voltage	VDS	20	V
Gate-Source Voltage	VGS	±12	V
Continuous Drain Current3	ID @TA=25℃	6	A
Continuous Drain Current3	ID @TA=70℃	5	A
Pulsed Drain Current1	IDM	25	A
Total Power Dissipation (TSSOP-8)	PD @TA=25℃	1	W
Total Power Dissipation (SOT-23-6)	PD @TA=25℃	0.5	W
Linear Derating Factor		0.008	W/℃

Note1: DW01M contains a circuit that will protect it from static discharge; but please take special care that no excessive static electricity or voltage which exceeds the limit of the protection circuit will be applied to it.

10. Electrical Characteristics

(Ta=25°C unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	Min	Typ	Max	UNIT
Supply Current	VCC=3.9V	ICC		3.0	6.0	μA
Overdischarge Current	VCC=2.0V	IOD		1.5	3	μA
Overcharge Protection Voltage	DW01MC-S、DW01MC-T	VOCP	4.25	4.30	4.35	V
Overcharge Release Voltage	DW01MC-S、DW01MC-T	VOCR	4.05	4.10	4.15	V
Overcharge Protection Voltage	DW01MC-SA、DW01MC-TA	VOCP	4.21	4.28	4.35	V
Overcharge Release Voltage	DW01MC-SA、DW01MC-TA	VOCR	4.01	4.08	4.15	V
Overdischarge Protection Voltage		VODP	2.30	2.40	2.50	V
Overdischarge Release Voltage		VODR	2.90	3.00	3.10	V
Overcurrent Protection Voltage		VOIP（VOI1）	120	150	180	mV
Short Current Protection Voltage	VCC=3.6V	VSIP（VOI2）	1.00	1.35	1.70	V
Overcharge Delay Time		TOC		80	200	ms
Overdischarge Delay Time	VCC=3.6V to 2.0V	TOD		40	100	ms
Overcurrent Delay Time (1)	VCC=3.6V	TOI1		10	20	ms
Overcurrent Delay Time (2)	VCC=3.6V	TOI2		100	200	μs
Charger Detection Threshold Voltage		VCH	-1.2	-0.7	-0.2	V
Minimum operating Voltage for 0V charging. *Note1	Voltage defined as VDD-CSI, VDD-VSS=0V	VST			1.5	V
			N-MOSFET			
Drain-Source Breakdown Voltage (BATT- to D12 / D12 to GND)	VGS=0V,ID=250uA	BVDSS	20			V
Breakdown Voltage Temperature Coefficient	Reference to 25℃, I_D=1mA	$\Delta BV_{DSS}/\Delta T_j$		0.1		V/℃
Static Source-Source On-Resistance (BATT- to GND)	VGS=3.7V,ID=1A	RSS(ON)		50	55	mΩ
	VGS=2.7V,ID=1A			60	70	mΩ
Static Source-Source On-Resistance (BATT- to GND) Only DW01MC-SA、TA	VGS=3.7V,ID=1A			55	60	mΩ
	VGS=2.7V,ID=1A			65	75	mΩ
Drain-Source Leakage Current) (BATT- to D12 / D12 to GND)	VDS=16V,VGS=0V	IDSS (Tj=25℃)			1	uA
Drain-Source Leakage Current (BATT- to D12 / D12 to GND)	VDS=16V,VGS=0V	IDSS (Tj=70℃)			25	uA

11. Description of Operation

Normal Condition

If VODP<VCC<VOCP and VCH<VCS<VOI1, M1 and M2 are both turned on. The charging and discharging processes can be operated normally.

Overcharge Protection

When the voltage of the battery cell exceeds the overcharge protection voltage (VOCP) beyond the overcharge delay time (TOC) period, charging is inhibited by turning off of the charge control MOSFET. The overcharge condition is released in two cases:

The voltage of the battery cell becomes lower than the overcharge release voltage (VOCR) through self-discharge.

The voltage of the battery cell falls below the overcharge protection voltage (VOCP) and a load is connected.
When the battery voltage is above VOCP, the overcharge condition will not release even a load is connected to the pack.

Overdischarge Protection

When the voltage of the battery cell goes below the overdischarge protection voltage (VODP) beyond the overdischarge delay time (TOD) period, discharging is inhibited by turning off the discharge control MOSFET.

The default of overdischarge delay time is 40ms. Inhibition of discharging is immediately released when the voltage of the battery cell becomes higher than overdischarge release voltage (VODR) through charging.

Overcurrent Protection

In normal mode, the DW01M continuously monitors the discharge current by sensing the voltage of CS pin. If the voltage of CS pin exceeds the overcurrent protection voltage (VOIP) beyond the overcurrent delay time (TOI1) period, the overcurrent protection circuit operates and discharging is inhibited by turning off the discharge control MOSFET. The overcurrent condition returns to the normal mode when the load is released or the impedance between BATT+ and BATT- is larger than 500kΩ. The DW01M provides two overcurrent detection levels (0.15V and 1.35V) with two overcurrent delay time (TOI1 and TOI2) corresponding to each overcurrent detection level.

Charge Detection after Overdischarge

When overdischarge occurs, the discharge control MOSFET turns off and discharging is inhibited. However, charging is still permitted through the parasitic diode of MOSFET. Once the charger is connected to the battery pack, the DW01M immediately turns on all the timing generation and detection circuitry. Charging progress is sensed if the voltage between CS and GND is below charge detection threshold voltage (VCH).

Auto Power Down recovery

The IC continues to operate even after the overdischarge state has been entered. The battery voltage rising to the overdischarge release voltage(VODR) or higher is the only required condition for the IC to return to the normal state.

12. Design Guide

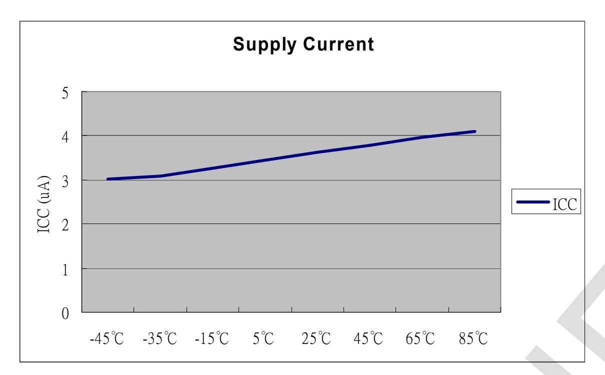
Suppressing the Ripple and Disturbance from Charger

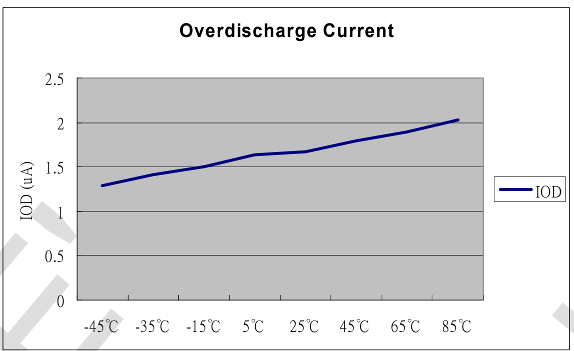
To suppress the ripple and disturbance from charger, connecting R1 and C1 to VCC is recommended.

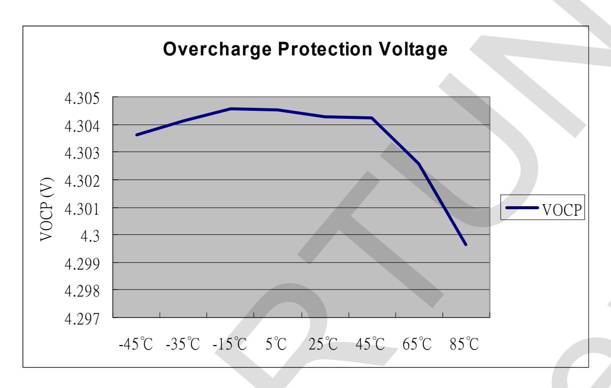
Protection the CS pin

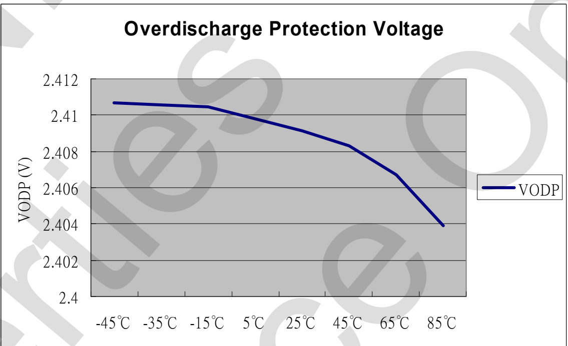
R2 is used for latch-up protection when charger is connected under overdischarge condition and overstress protection at reverse connecting of a charger.

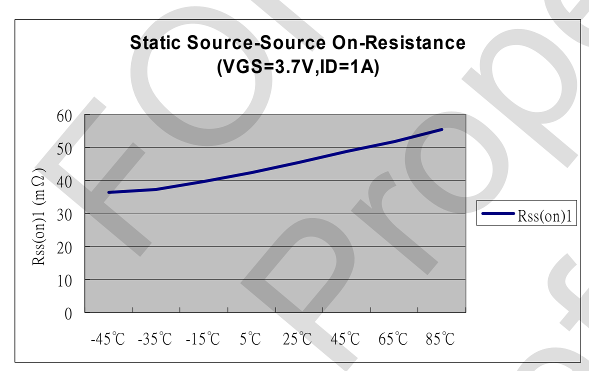
13. Typical Operating Characteristics

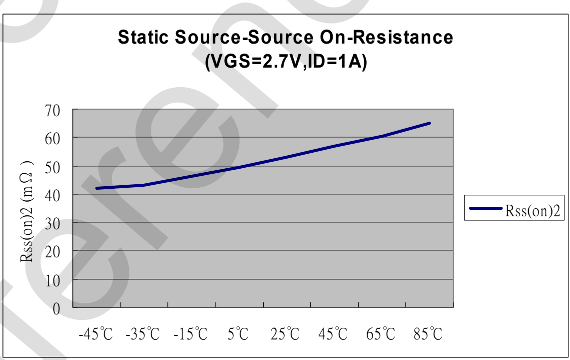


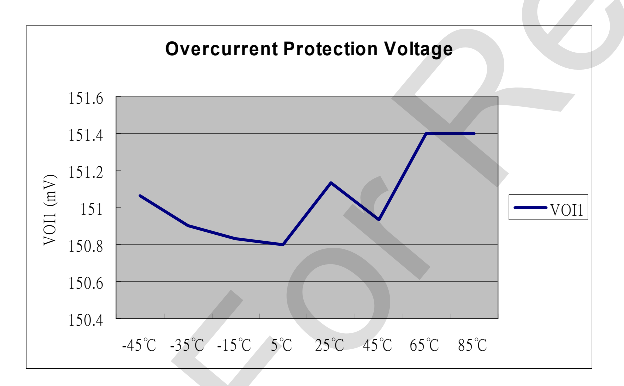






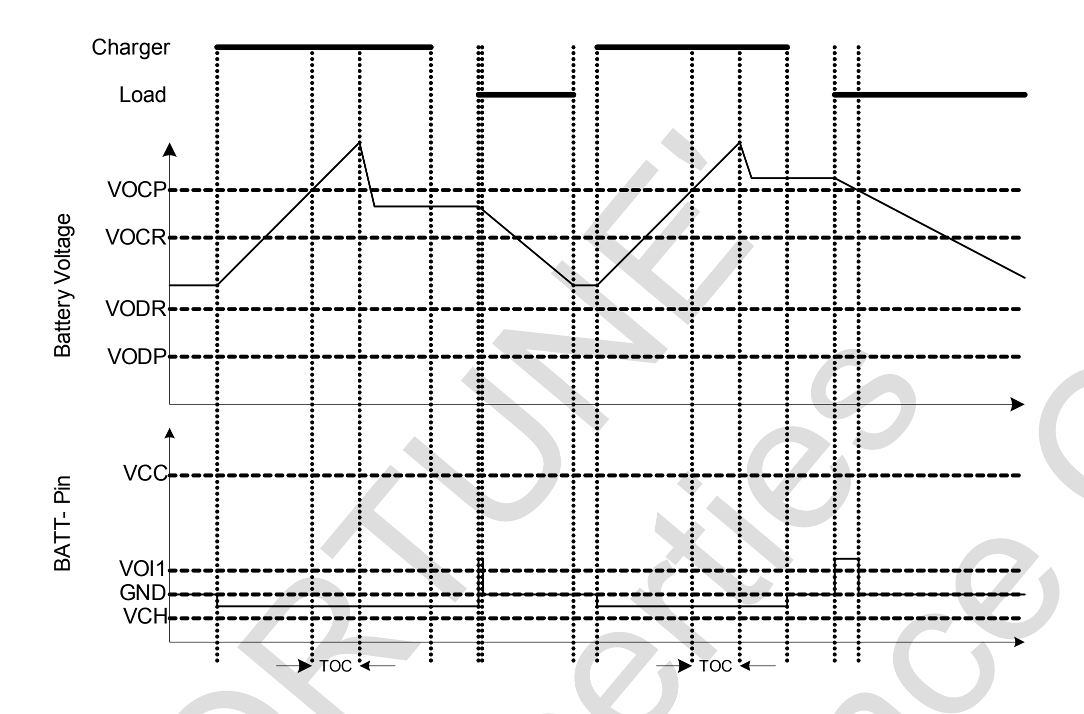




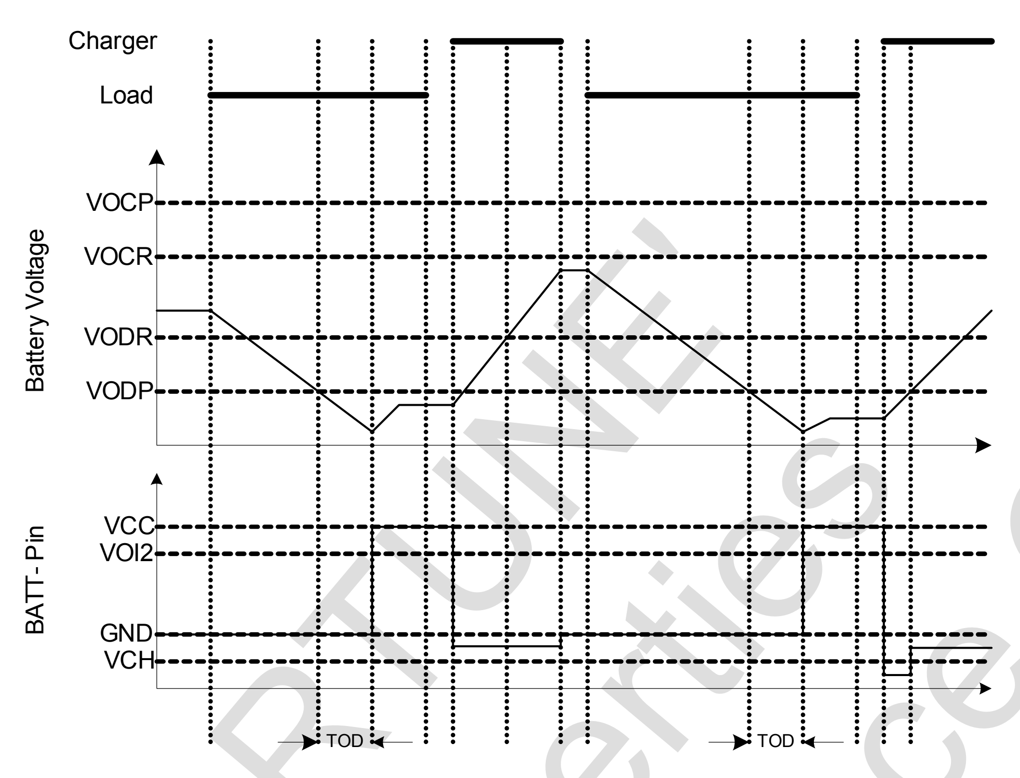


14. Timing Diagram

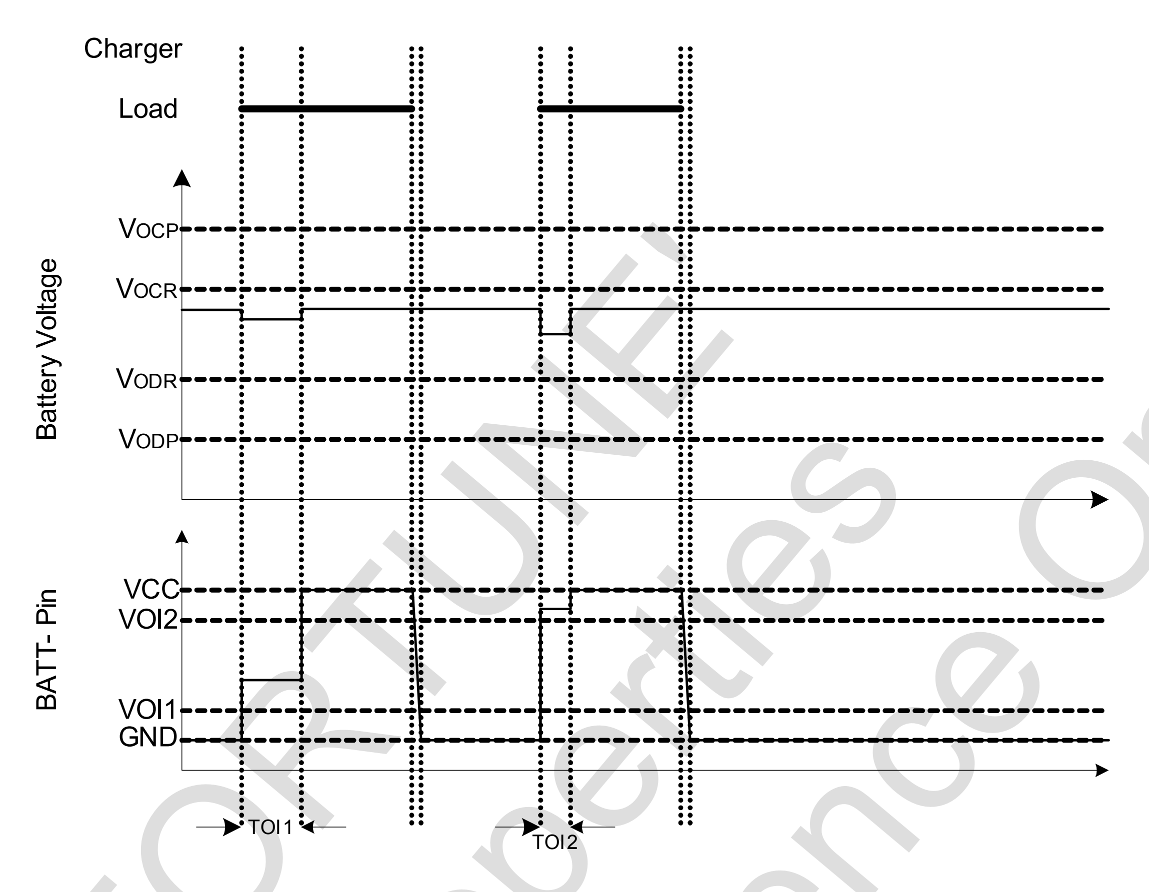
Overcharge Condition →Load Discharging → Normal Condition



Overdischarge Condition → Charging by a Charger →Normal Condition

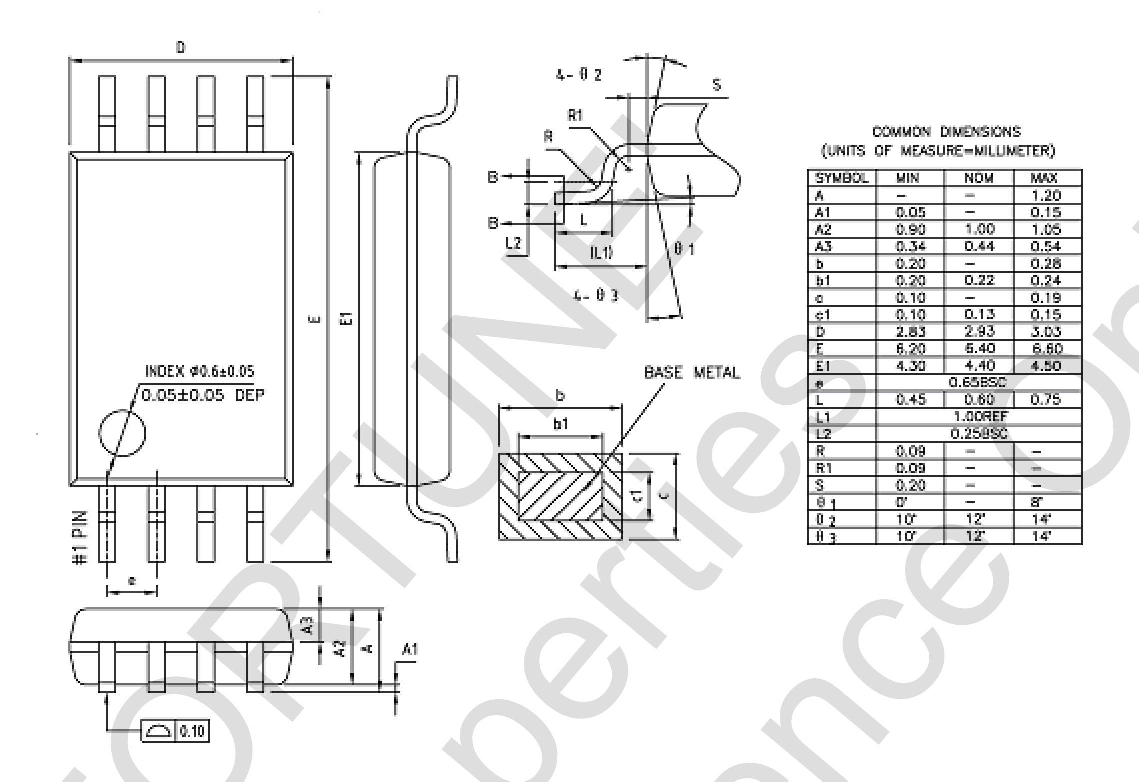


Over Current Condition → Normal Condition



15. Package Outline

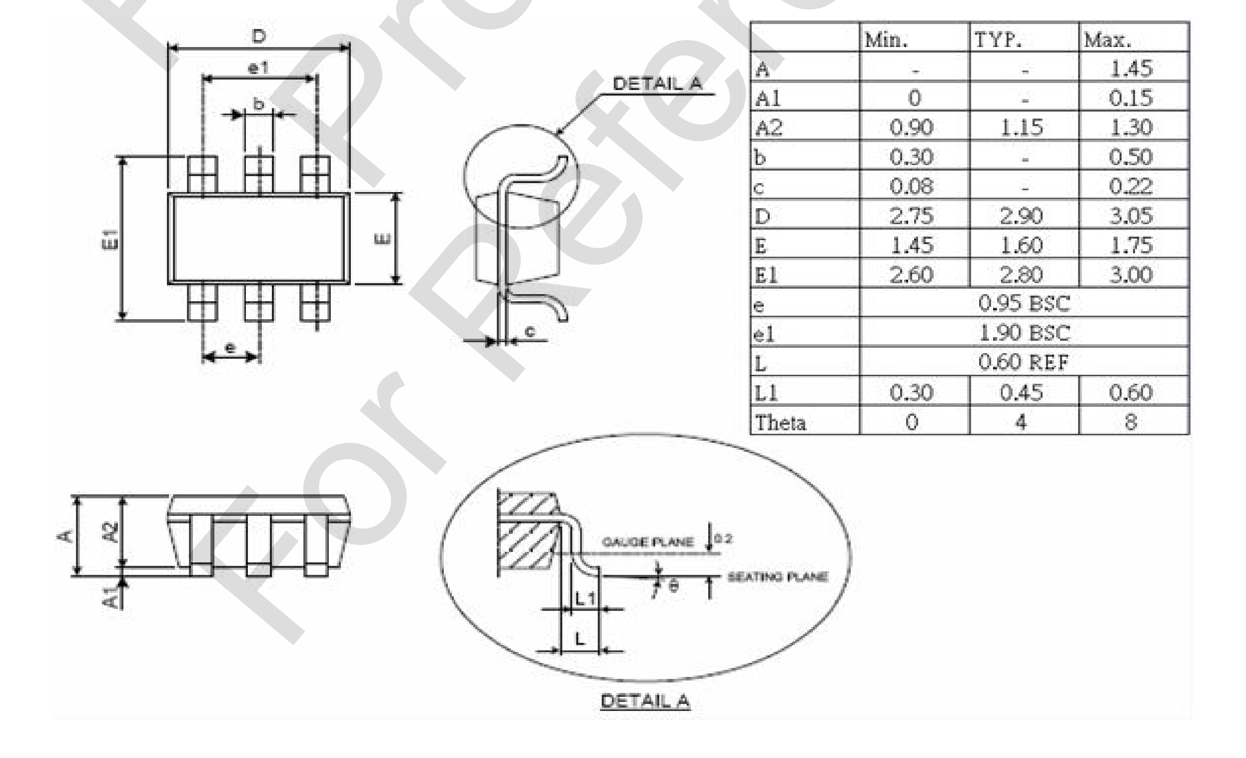
Dimension (TSSOP-8)



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	–	–	1.20
A1	0.05	–	0.15
A2	0.90	1.00	1.05
A3	0.34	0.44	0.54
b	0.20	–	0.28
b1	0.20	0.22	0.24
c	0.10	–	0.19
c1	0.10	0.13	0.15
D	2.83	2.93	3.03
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
e	0.65BSC		
L	0.45	0.60	0.75
L1	1.00REF		
L2	0.25BSC		
R	0.09	–	–
R1	0.09	–	–
S	0.20	–	–
θ 1	0°	–	8°
θ 2	10°	12°	14°
θ 3	10°	12°	14°

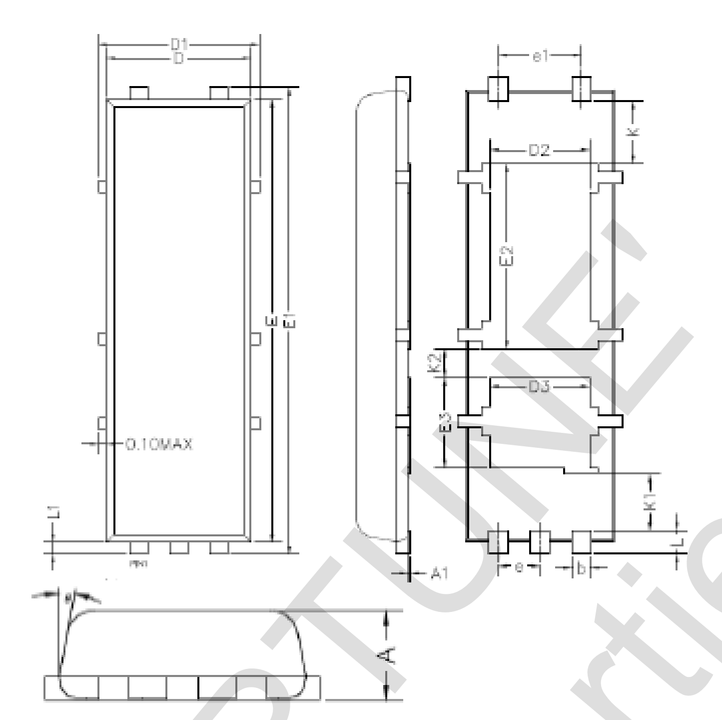
Dimension (SOT-23-6)

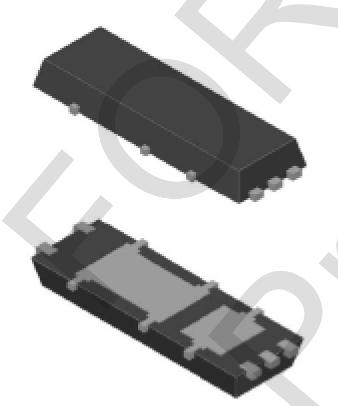


	Min.	TYP.	Max.
A	-	-	1.45
A1	0	-	0.15
A2	0.90	1.15	1.30
b	0.30	-	0.50
c	0.08	-	0.22
D	2.75	2.90	3.05
E	1.45	1.60	1.75
E1	2.60	2.80	3.00
e	0.95 BSC		
e1	1.90 BSC		
L	0.60 REF		
L1	0.30	0.45	0.60
Theta	0	4	8



Dimension (DFN-5)





Note:
All dimensions do not include mold flash, gate burrs or protrusions.

Unit: mm

Symbol	Min.	TYP.	Max.
A	0.58	0.63	0.68
A1	0.00	0.02	0.05
b	0.18	0.23	0.28
D	1.70	1.8	1.90
E	5.40	5.50	5.60
D1	-	-	2.10
E1	5.70	5.80	5.90
D2	1.10	1.20	1.30
E2	2.10	2.25	2.35
D3	1.10	1.20	1.30
E3	1.00	1.10	1.20
e		0.50 BSC	
e1		1.00 BSC	
K	0.67	0.77	0.87
K1	0.59	0.69	0.79
K2	0.25	0.35	0.45
L	0.25	0.35	0.45
L1		0.15 BSC	
θ	10°	12°	14°

16. Revision History

Version	Date	Page	Description
1.0	2010/09/21	All	New release
1.1	2010/10/14	3,8	Ordering Information add Tx - serial code for MOSFET Electrical Characteristics add RSS(ON) DW01Mx-T1 only
1.2	2010/12/15	3,8	Revise RSS(ON) 3.7V Typ：50mΩ、MAX：55mΩ RSS(ON) 2.7V TYP：60mΩ、MAX：70mΩ
1.3	2011/04/13	4	Add Typical Operating characteristics Add DW01MC-SA、DW01MC-TA
1.4	2011/05/19	5,6	Revise Typical Application Circuit information Add package DFN-5
1.5	2011/09/08	15	Revise Package Outline
1.6	2012/07/19	16	Revise Package Outline (DFN-5)
1.7	2012/09/12	16	Revise Package Outline (DFN-5)
1.8	2012/10/12	8	Revise TOI2 Specified
1.9	2014/05/22	2	Revise company address